

FIG.1

DEFINITION	
APPOINTMENT 19	bw_remaining 13
INITIAL VALUE	
0	4915
READ-IN VALUE	
0	DIRECTLY PREVIOUS SUCCESSFUL LOCK
EFFECT OF LOCK	
DISREGARDED	CONDITIONAL WRITING

FIG.2

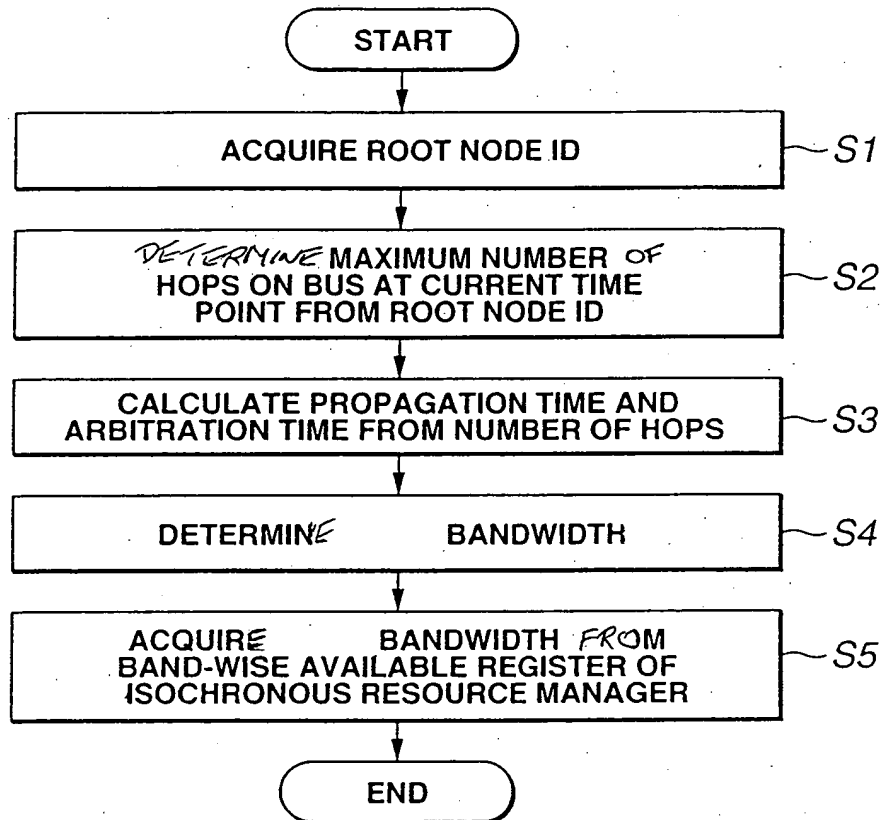


FIG.3

TRANSMIT FIRST

10	phy_ID	0	L	gap_cnt	sp	del	c	pwr	p0	p1	p2	i	m
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TRANSMIT FIRST

10	phy_ID	1	n	rsv	pa	pb	pc	pd	pe	pf	pg	ph	rm
----	--------	---	---	-----	----	----	----	----	----	----	----	----	----

LOGICAL INVERSION OF FIRST QUADLET

SELF-ID
TRANSMIT LAST
PACKET #1, #2, #3

	n	pa	pb	pc	pd	pe	pf	pg	ph
PACKET #1	0	p3	p4	p5	p6	p7	p8	p9	p10
PACKET #2	1	p11	p12	p13	p14	p15	p16	p17	p18
PACKET #3	2	p19	p20	p21	p22	p23	p24	p25	p26

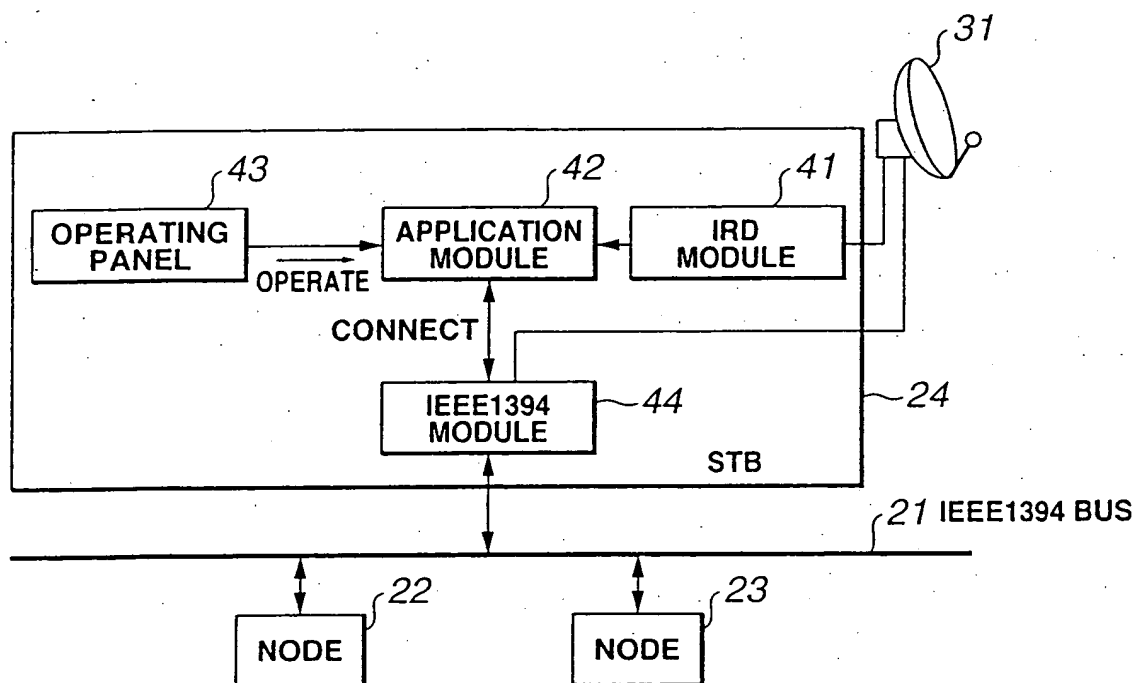
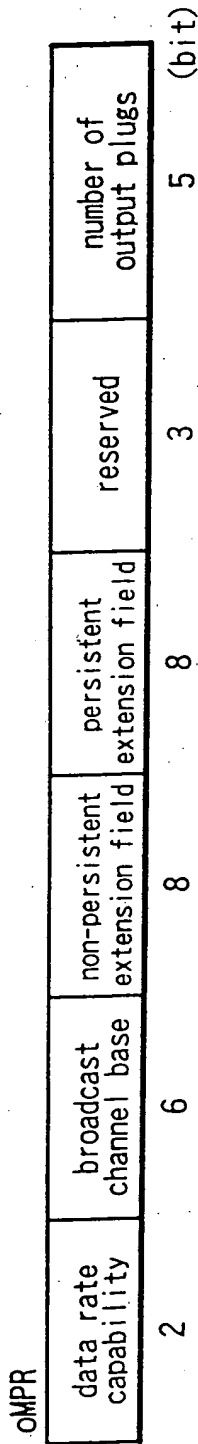


FIG.5

FIG. 6

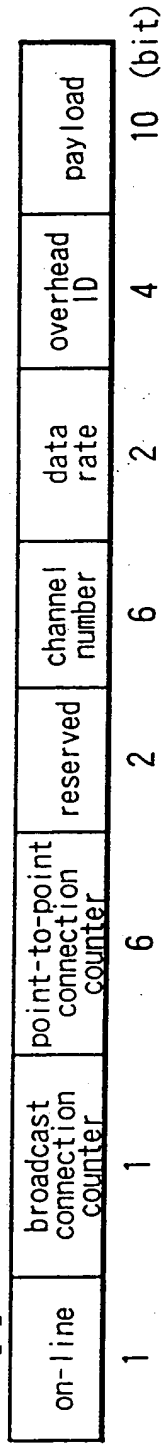
900h	Output Master Plug Register
904h	Output Plug Control Register #0
908h	Output Plug Control Register #1
⋮	⋮
97Ch	Output Plug Control Register #30
980h	Input Master Plug Register
984h	Input Plug Control Register #0
988h	Input Plug Control Register #1
⋮	⋮
9FCh	Input Plug Control Register #30

FIG. 7A



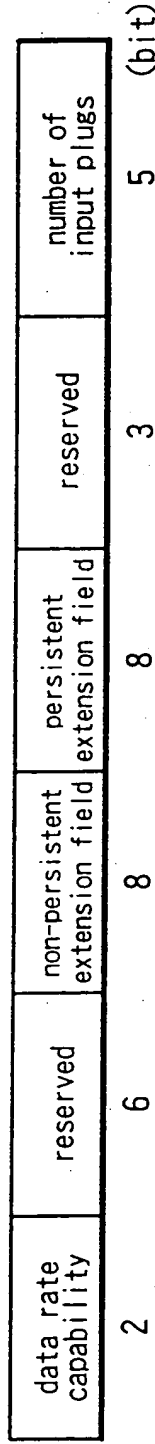
oPCR[n]

FIG. 7B



iMPR

FIG. 7C



iPCR[n]

FIG. 7D

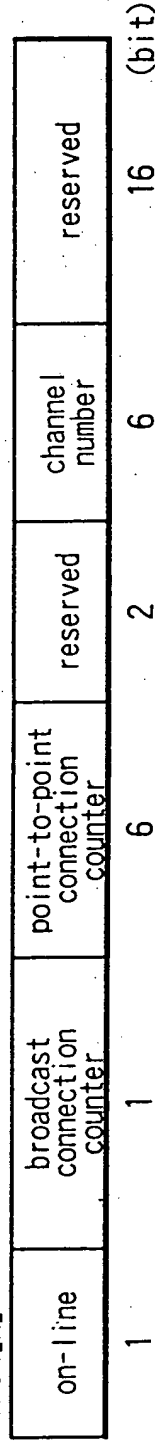


FIG. 8

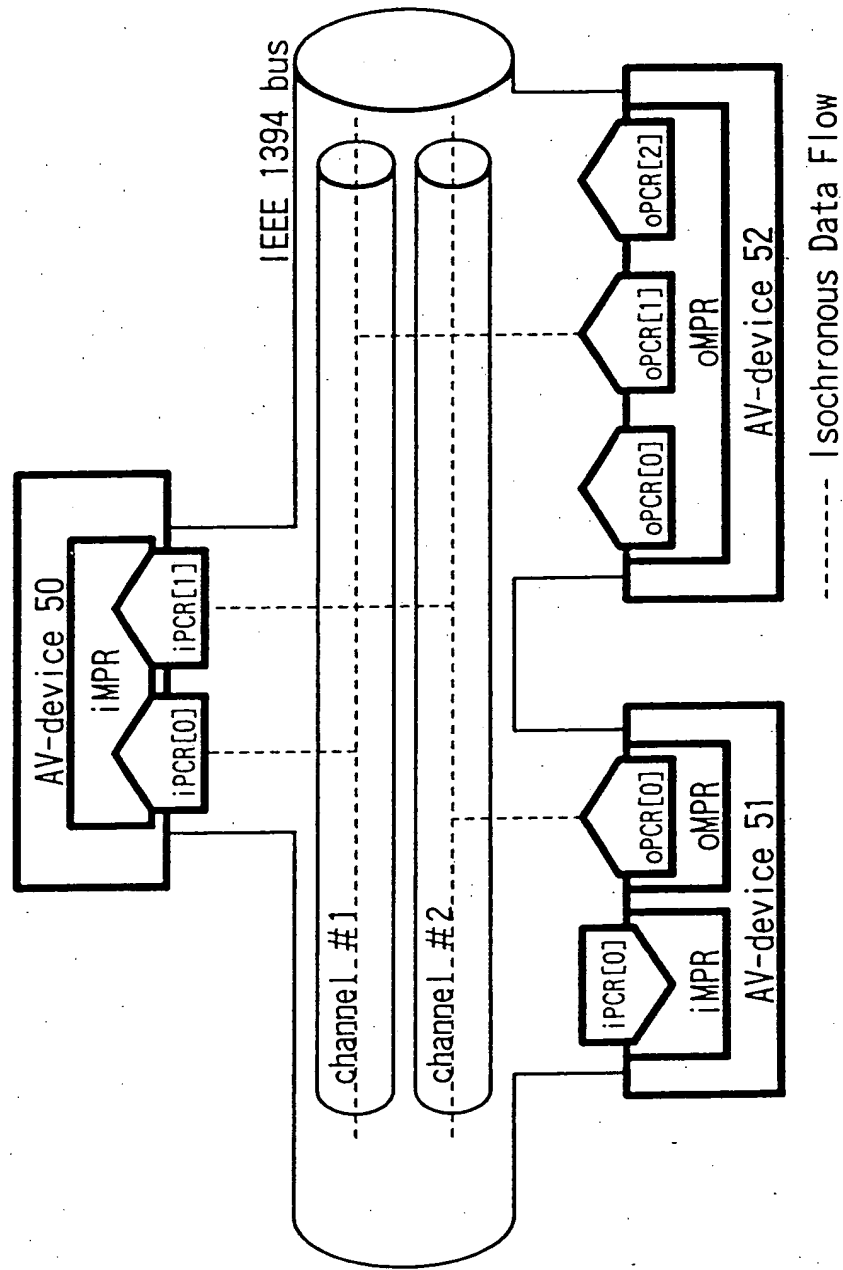


FIG. 9

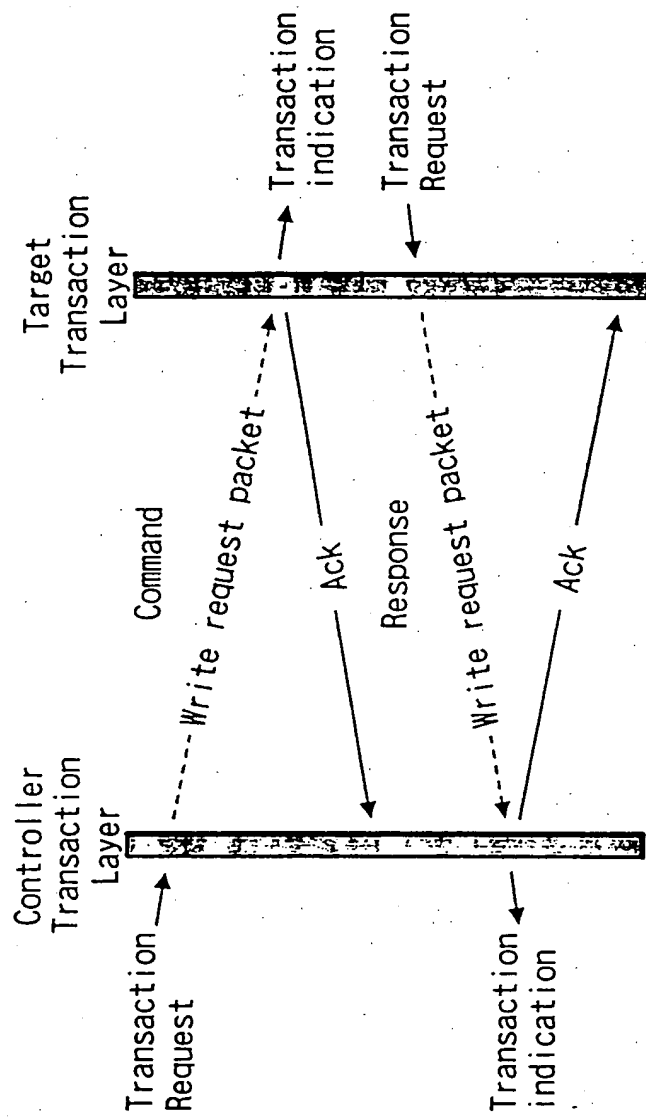


FIG. 10

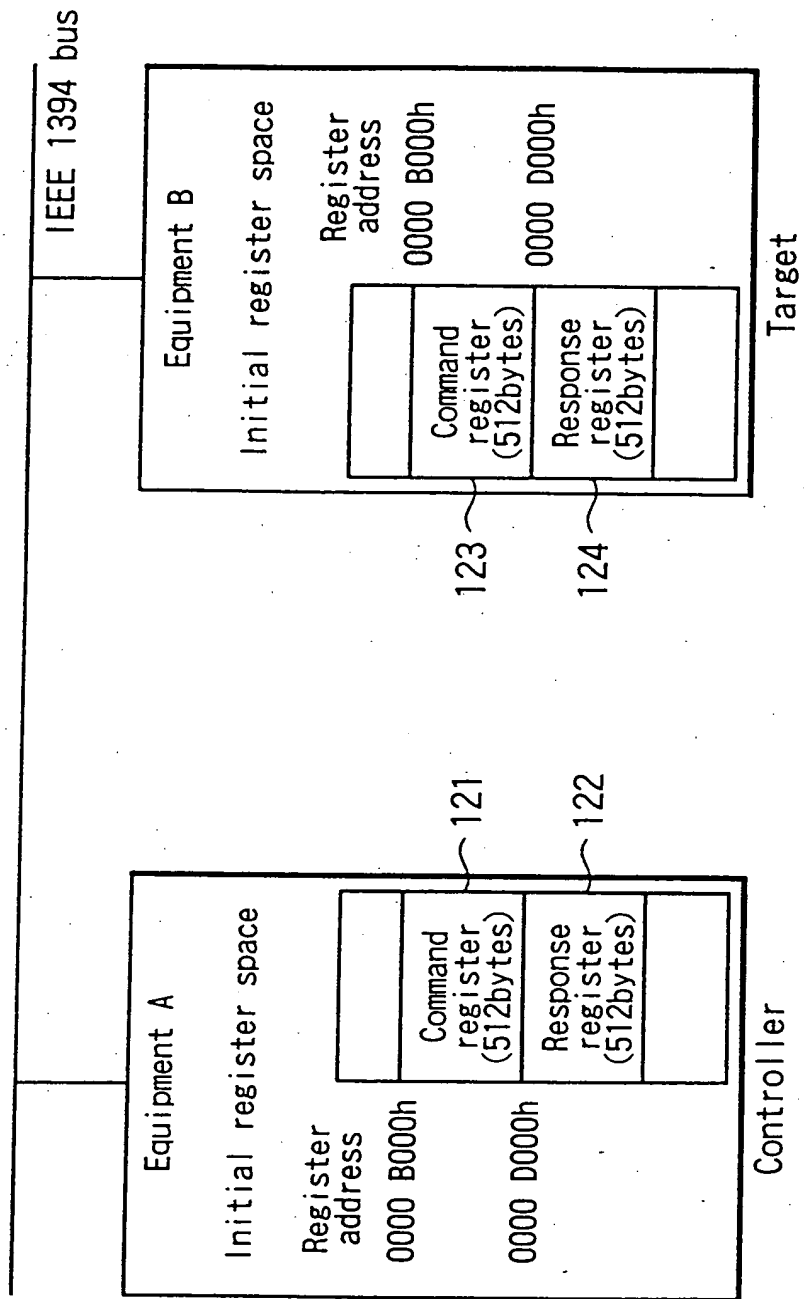


FIG. 11

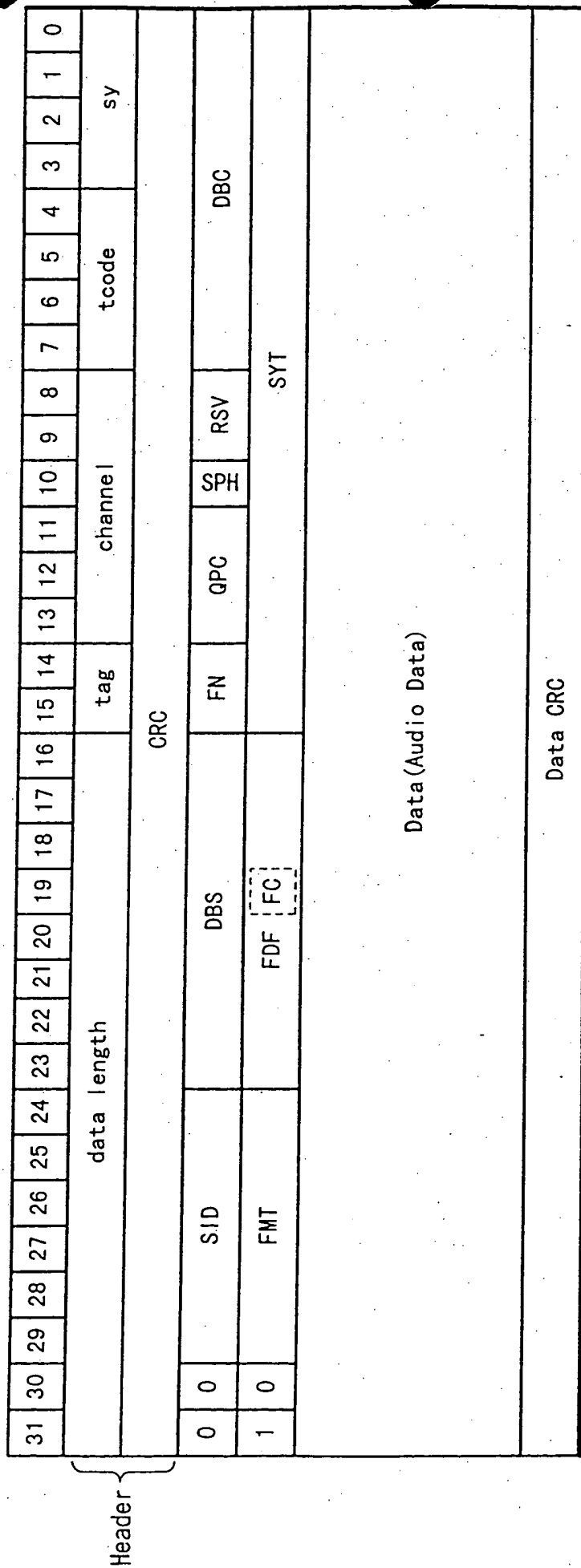
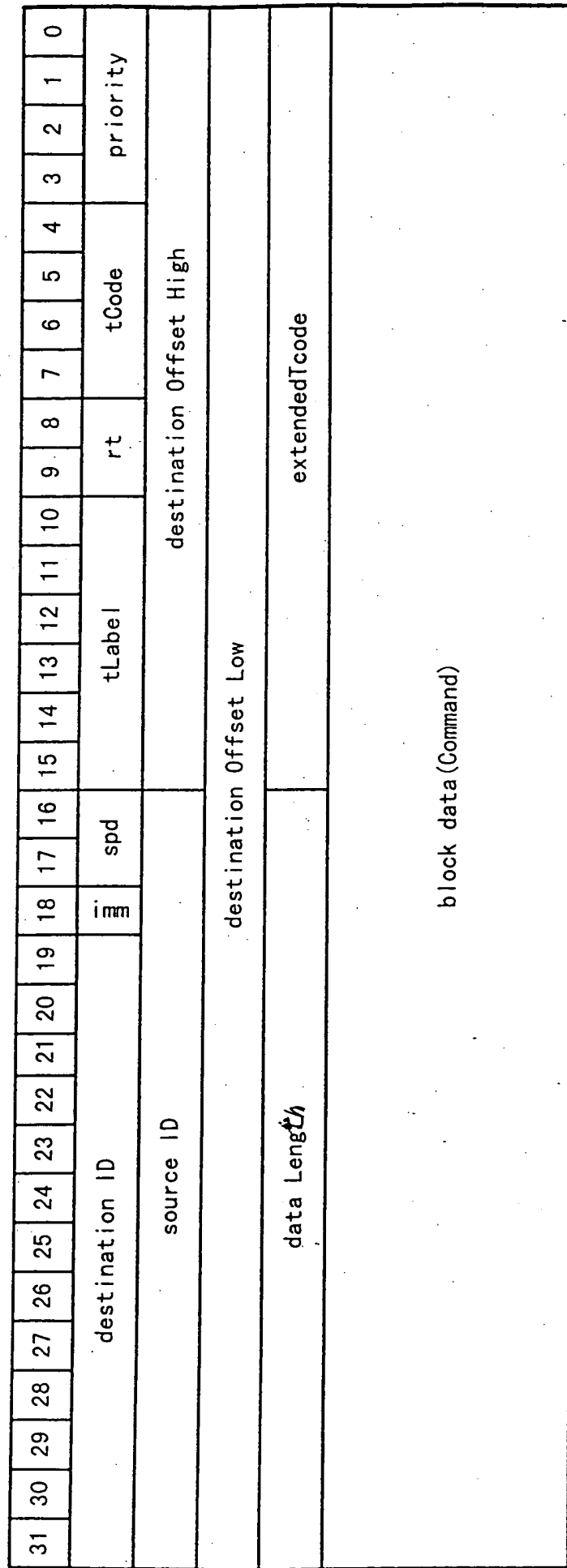


FIG. 12



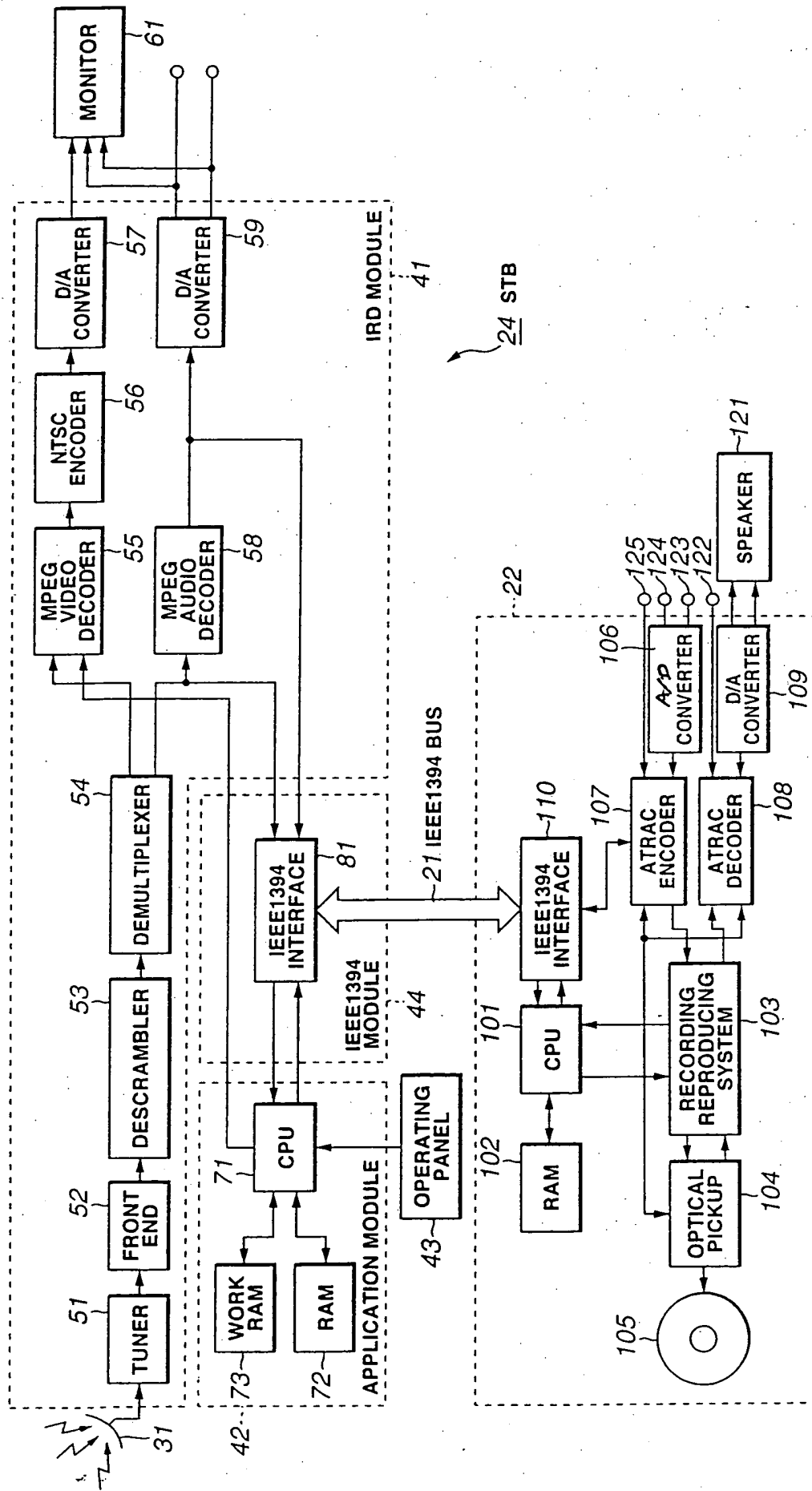


FIG. 13

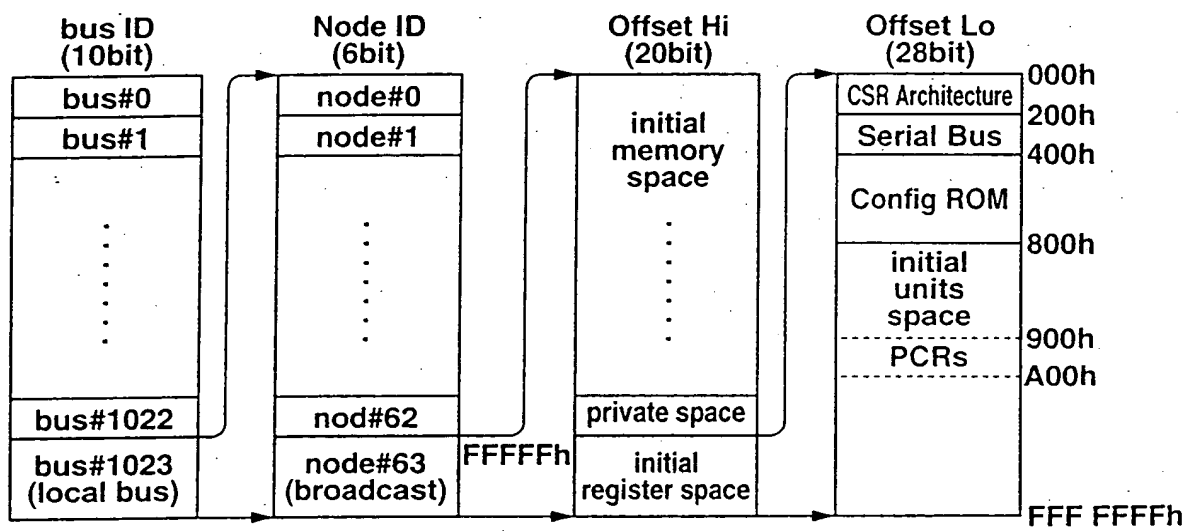


FIG.14

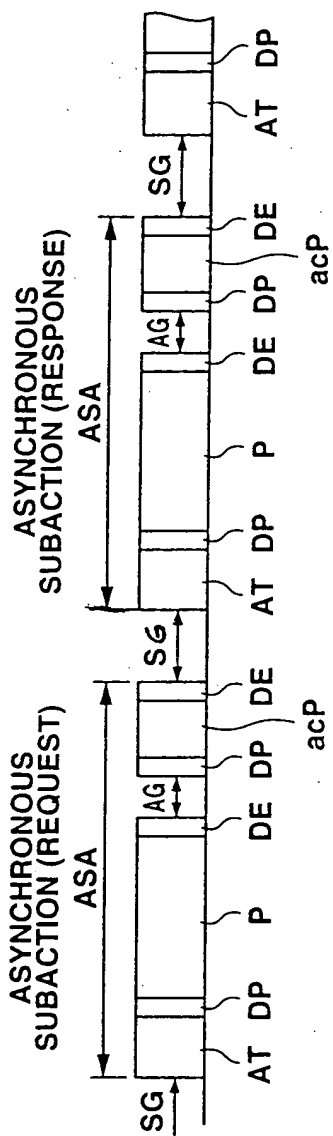


FIG. 15

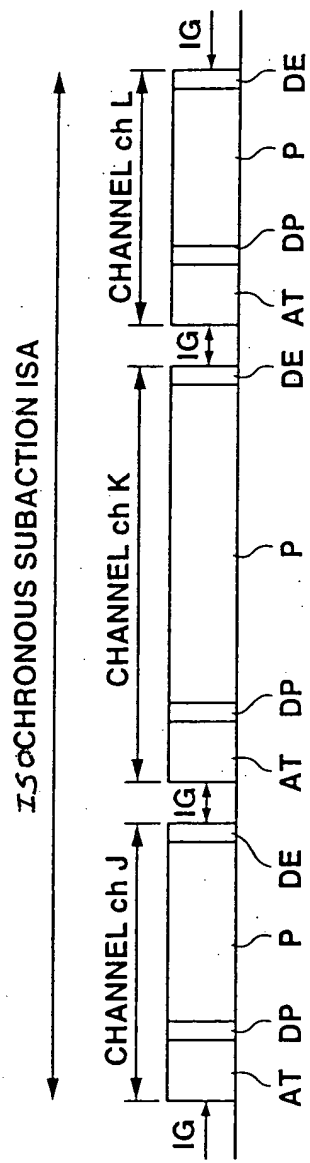


FIG. 16

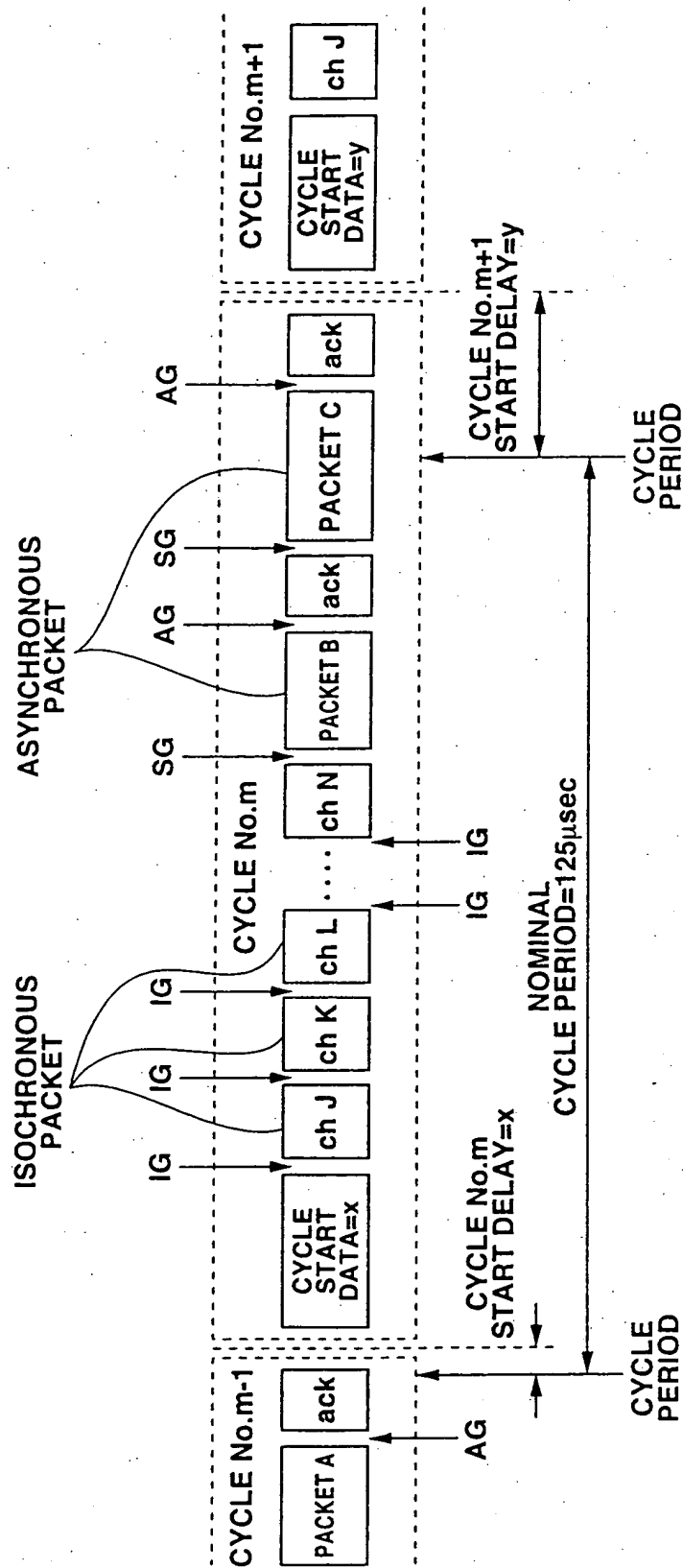


FIG.17

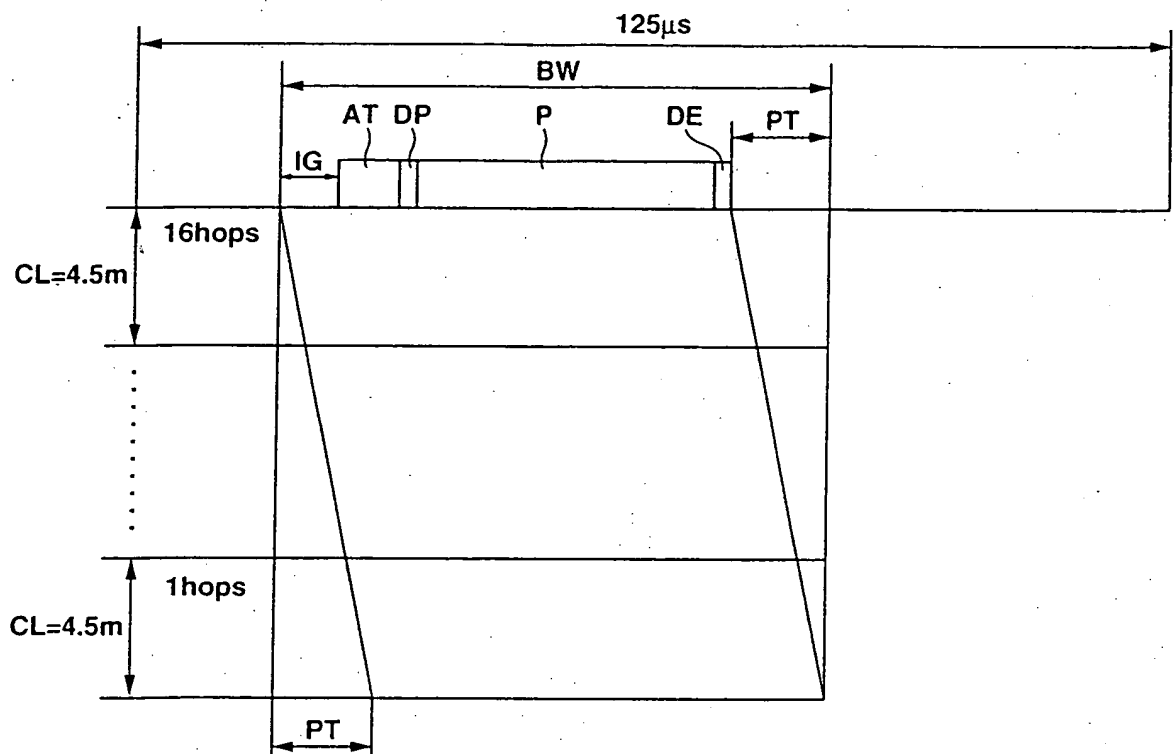


FIG.18



FIG. 19